

Day: Thursday Date: 10/19/2006

Time: 14:50:01

Inventor Name Search Result

Your Search was:

Last Name = PARK

First Name = HONG-JUNE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08626817	5729178	250	04/03/1996	FULLY DIFFERENTIAL FOLDED CASCODE CMOS OPERATIONAL AMPLIFIER HAVING ADAPTIVE BIASING AND COMMON MODE FEEDBACK CIRCUITS	PARK, HONG- JUNE
08891153	5929654	250	07/10/1997	TEMPERATURE-INSENSITIVE CURRENT CONTROLLED CMOS OUTPUT DRIVER	PARK, HONG- JUNE
08891154	5880625	150	07/10/1997	TEMPERATURE INSENSITIVE CONSTANT CURRENT GENERATOR	PARK, HONG- JUNE
09545461	6275066	150	04/07/2000	Current-mode bidirectional input/output buffer for impedance matching	PARK, HONG- JUNE
09547863	Not Issued	161	04/12/2000	Current sense amplifier-based CMOS flip-flop of enhanced output transitioin speed	PARK, HONG- JUNE
09726413	6356501	150	12/01/2000	APPARATUS FOR GENERATING HIGH VOLTAGE SIGNAL	PARK, HONG- JUNE
09832869	6816877	150	04/12/2001	APPARATUS FOR DIGITAL MULTIPLICATION USING REDUNDANT BINARY ARITHMETIC	PARK, HONG- JUNE
09950817	6504407	150	09/13/2001	PROGRAMMABLE HIGH SPEED FREQUENCY DIVIDER	PARK, HONG- JUNE
10347795	6803791	150		EQUALIZING RECEIVER WITH DATA TO CLOCK SKEW COMPENSATION	PARK, HONG- JUNE
10667658	Not Issued	30	09/23/2003	Look-ahead decision feedback equalizing receiver	PARK, HONG- JUNE
11020771	Not	61	12/21/2004	Slew rate controlled output driver	PARK, HONG-

·	Issued		for use in semiconductor device	JUNE
11451921	Not Issued	30	Bubble error rejecter in data converter	PARK, HONG- JUNE

Inventor Search Completed: No Records to Display.

Saarah Anathari Invanta	Last Name	First Name	
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Inventor Name Search Result

Your Search was:

Last Name = SOHN

First Name = YOUNG-SOO

Application# Patent# Status Date Filed Title Inventor Name					
		=			Inventor Name
<u>10347795</u>	6803791	150	01/22/2003	EQUALIZING RECEIVER WITH DATA TO CLOCK SKEW COMPENSATION	SOHN, YOUNG- SOO
10667658	Not Issued	30	1	Look-ahead decision feedback equalizing receiver	SOHN, YOUNG- SOO
10804530	Not Issued	93	03/19/2004	MEMORY DEVICE FOR COMPENSATING FOR A CLOCK SKEW CAUSING A CENTERING ERROR AND A METHOD OF COMPENSATING FOR THE CLOCK SKEW	SOHN, YOUNG- SOO
<u>10947576</u>	Not Issued	41	09/22/2004	Fully integrated protein lab-on-a- chip with smart microfluidics for spot array generation	SOHN, YOUNG- SOO
11040808	Not Issued	30	01/21/2005	Decision feedback equalization input buffer	SOHN, YOUNG- SOO
11082497	Not Issued	95	03/17/2005	PHASE LOCKED LOOP WITH ADAPTIVE LOOP BANDWIDTH	SOHN, YOUNG- SOO
11259545	Not Issued	30	10/25/2005	Phase and delay locked loops and semiconductor memory device having the same	SOHN, YOUNG- SOO
11345451	Not Issued	30	02/02/2006	Receiving apparatus and method thereof	SOHN, YOUNG- SOO
11350868	Not Issued	30	02/10/2006	Charge pump circuit for semiconductor memory device	SOHN, YOUNG- SOO
11487545	Not Issued	30	07/14/2006	Process-insensitive self-biasing phase locked loop circuit and self-biasing method thereof	SOHN, YOUNG- SOO
60179292	Not Issued	159	01/31/2000	System for transferring fluid samples through a sensor array	SOHN, YOUNG- SOO
60506321	Not Issued	159	09/26/2003	Biochip and methods for fabricating and reading same	SOHN, YOUNG- SOO

Inventor Search Completed: No Records to Display.

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